Electronic Acknowledgement Receipt					
EFS ID:	1362645				
Application Number:	10699764				
International Application Number:					
Confirmation Number:	4234				
Title of Invention:	In-circuit configuration architecture for embedded configurable logic array				
First Named Inventor/Applicant Name:	Albert Sun				
Customer Number:	22470				
Filer:	Mark Andrew Haynes/Paula Hurley				
Filer Authorized By:	Mark Andrew Haynes				
Attorney Docket Number:	MXIC 1520-1				
Receipt Date:	08-DEC-2006				
Filing Date:	03-NOV-2003				
Time Stamp:	16:51:14				
Application Type:	Utility				

Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$120
RAM confirmation Number	484
Deposit Account	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)	
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1		efsresponse20061208.pdf	238150	yes	6				
	Multipart Description/PDF files in .zip description								
	Document Description		Start	End					
	Amendment - After Non-Final Rejection		1	1					
	Applicant Arguments/Remarks Made in an Amendment		2	6					
Warnings:									
Information:									
2	Fee Worksheet (PTO-875)	fee-info.pdf	8175	no	2				
Warnings:									
Information:									
	Total Files Size (in bytes): 246325								

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.